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a feedback path to provide an output of said pulse generator to said activation circuit, said feedback path including an inverter to create a high signal in response to a low signal on said feedback path.

Please cancel claims 19, 21, 30 and 31-43.

REMARKS


Since claim 19 was indicated to be allowable, claim 11 has been amended to include the subject matter of claim 19.

Therefore, the application should now be in condition for allowance, and the Examiner's prompt action in accordance therewith is respectfully requested.

Respectfully submitted,

Date:

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APPENDIX

11. (Twice Amended). An integrated circuit comprising:

an activation circuit to determine whether a supply voltage reaches a predetermined level, said activation circuit including an inverter coupled to the gate of a load transistor, a second transistor coupled to said load transistor and a third transistor coupled between said load transistor and said first transistor;

a pulse generator to generate pulses to indicate that a supply voltage is ramping up and to terminate the generation of the pulses after the supply voltage reaches a predetermined level; and

a feedback path to provide an output of said pulse generator to said activation circuit, said feedback path including an inverter to create a high signal in response to a low signal on said feedback path.

Please cancel claims 19, 21-30 and 31-43.